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08134 7590 03/04/2006

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Kristin R. Hayes (Depositor's name)
Kristin R. Hayes (Signature)
July 31, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/777,128	02/13/2004	Till Schloesser	DNF-135	3801

TITLE OF INVENTION: ARCHITECTURE FOR VERTICAL TRANSISTOR CELLS AND TRANSISTOR-CONTROLLED MEMORY CELLS

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$0	\$0	\$0	08/04/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
HUYMIL ANDY	2818	257-302000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.303).

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Slater & Matsil, L.L.P.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

Infineon Technologies AG

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims **SMALL ENTITY** status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming **SMALL ENTITY** status. See 37 CFR 1.27(g)(7).

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Authorized Signature

Ira S. Matsil

Date **July 31, 2006**

Typed or printed name

Registration No. **35,272**

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